

This listing of claims will replace all prior versions, and listings, of claims in the application:

**LISTING OF THE CLAIMS:**

Claim 1 (Currently Amended) A vertically stacked coplanar transmission line structure for an integrated circuit (IC) chip defining a closed ground return path within the transmission line structure, comprising:

a micro-strip pair of first and second vertically stacked coplanar conductors, each first and second vertical stack comprising a metal layer, a next metal layer down, and an intermediate connecting via layer in between the metal layer and the next metal layer down, said intermediate connecting via layer comprising a via bar having a width dimension approximately equal to a width dimension of said first and second vertically stacked coplanar conductors and having a length dimension approximately equal to a length dimension of said first and second vertically stacked coplanar conductors.

Claim 2 (Original) The transmission line structure of claim 1, wherein each vertically stacked coplanar conductor comprises metal in the metal layer m(i), metal in the next metal layer down m(i-1), and metal in the intermediate connecting via layer.

Claim 3 (Original) The transmission line structure of claim 1, fabricated in upper metal layers of the IC chip.

Claim 4 (Original) The transmission line structure of claim 1, wherein the intermediate connecting via layer comprises a single via bar which extends across an entire width of the intermediate connecting via layer.

Claim 5 (Original) The transmission line structure of claim 1, wherein the intermediate connecting via layer comprises a plurality of long parallel via bars spaced apart across a width of the intermediate connecting via layer.

Claim 6 (Original) The transmission line structure of claim 5, wherein the plurality of long parallel via bars are positioned to be close to an inside edge of the coplanar vertically stacked conductor which faces the other coplanar vertically stacked conductor in the transmission line structure.

Claim 7 (Original) The transmission line structure of claim 1, wherein the micro-strip pair of first and second vertically stacked coplanar conductors comprise a differential positive and negative pair of transmission line conductors.

Claim 8 (Original) The transmission line structure of claim 1, wherein the micro-strip pair of first and second vertically stacked coplanar conductors comprise signal and ground transmission line conductors.

Claim 9 (Original) The transmission line structure of claim 1, further comprising a third vertically stacked coplanar conductor comprising a metal layer, a next metal layer down, and an intermediate connecting via layer in between the metal layer and the next metal layer down, and

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the first, second and third vertically stacked coplanar conductors comprise respectively ground, signal and ground conductors of a waveguide transmission line structure.

Claim 10 (Original) The transmission line structure of claim 1, further comprising third and fourth vertically stacked coplanar conductors, each comprising a metal layer, a next metal layer down, and an intermediate connecting via layer in between the metal layer and the next metal layer down, and the first, second, third and fourth vertically stacked coplanar conductors comprise respectively a ground, a differential positive and negative pair of transmission line conductors and a ground of a waveguide transmission line structure.

Claim 11 (Original) A vertically stacked coplanar transmission line structure for an integrated circuit (IC) chip defining a closed ground return path within the transmission line structure, comprising:

a micro-strip pair of first and second vertically stacked coplanar conductors, each comprising a metal layer, a next metal layer down, a second next metal layer down, a first intermediate connecting via layer in between the metal layer and the next metal layer down, and a second intermediate connecting via layer in between the next metal layer and the second next metal layer down.

Claim 12 (Original) The transmission line structure of claim 11, wherein each vertically stacked coplanar conductor comprises metal in the metal layer  $m(i)$ , metal in the next metal layer down  $m(i-1)$ , metal in the second next metal layer down  $m(i-2)$ , metal in the first intermediate connecting via layer, and metal in the second intermediate connecting via layer

Claim 13 (Original) The transmission line structure of claim 11, fabricated in upper metal layers of the IC chip.

Claim 14 (Original) The transmission line structure of claim 11, wherein the first intermediate connecting via layer and the second intermediate connecting via layer each comprises a single via bar which extends across an entire width of the intermediate connecting via layer.

Claim 15 (Original) The transmission line structure of claim 11, wherein the first intermediate connecting via layer and the second intermediate connecting via layer each comprises a plurality of long parallel via bars spaced apart across a width of the intermediate connecting via layer.

Claim 16 (Original) The transmission line structure of claim 15, wherein the plurality of long parallel via bars are positioned to be close to an inside edge of the coplanar vertically stacked conductor which faces the other coplanar vertically stacked conductor in the transmission line structure.

Claim 17 (Original) The transmission line structure of claim 11, wherein the micro-strip pair of first and second vertically stacked coplanar conductors comprise a differential positive and negative pair of transmission line conductors.

Claim 18 (Original) The transmission line structure of claim 11, wherein the micro-strip pair of first and second vertically stacked coplanar conductors comprise signal and ground transmission line conductors.

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Claim 19 (Original) The transmission line structure of claim 11, further comprising a third vertically stacked coplanar conductor comprising a metal layer, a next metal layer down, a second next metal layer down, a first intermediate connecting via layer in between the metal layer and the next metal layer down, and a second intermediate connecting via layer in between the next metal layer and the second next metal layer down, and the first, second and third vertically stacked coplanar conductors comprise respectively ground, signal and ground conductors of a waveguide transmission line structure.

Claim 20 (Original) The transmission line structure of claim 11, further comprising third and fourth vertically stacked coplanar conductors, each comprising a metal layer, a next metal layer down, a second next metal layer down, a first intermediate connecting via layer in between the metal layer and the next metal layer down, and a second intermediate connecting via layer in between the next metal layer and the second next metal layer down, and the first, second, third and fourth vertically stacked coplanar conductors comprise respectively a ground, a differential positive and negative pair of transmission line conductors and a ground of a waveguide transmission line structure.

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